

ABSTRACT

A method and a jitter buffer regulating circuit for regulating a jitter buffer

Transmission delays due to the buffering of data packets are registered for the purpose of regulating a jitter buffer. Weighted mean delay values are continuously derived from registered transmission delays, with a shorter transmission delay receiving a higher weighting than a longer transmission delay. A read-out speed of the jitter buffer is then regulated as a function of the continuously derived weighted mean delay values in such a way that the weighted mean delay values are adjusted as a regulating variable to a predefined desired delay.